

# Conference Program at a Glance

## Pre-conference Schedule

Sunday 14<sup>th</sup> of December 2008

8:00-16:00	<b>Registration</b>	M9
<b>Time</b>	<b>Event/Session</b>	<b>Location</b>
<b>Morning Tutorials</b>		
8:30-12:00	Tutorial 1: Low Power, Robust SRAMs for Nano- Metric Technologies	M9-006
	Tutorial 2: Pushing the limits of energy consumption: opportunities and challenges	Biruni Hall (M9-007)
12:00-13:30	<b>Break</b>	
<b>Afternoon Tutorials</b>		
13:30-17:00	Tutorial 3: Design Methodology of Multi-core, GHz Processor Designs in 45nm Technology	M9-008
	Tutorial 4: Large Area Digital Imaging Technology	Biruni Hall (M9-007)

## Conference Schedule

Monday 15<sup>th</sup> of December 2008

8:00-16:00	<b>Registration</b>	M9
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Time	Event/Session	Location
10:00-11:00	<b>Opening Ceremony</b>	Al Zuhri Hall M2
11:00-11:45	Keynote 1 “Power Management for VLSI: The History and the Outlook”	Al Zuhri Hall M2
11:45-12:00	<b>Morning Coffee Break</b>	
	<b>Sessions</b>	
12:30-13:40	Session 1: Analog Circuit Techniques (I)	M9-006
	Session 2: Digital Signal and Data Processing	Biruni Hall (M9-007)
	Session 3: Digital VLSI Design	M9-008
13:40:14:40	<b>Lunch</b>	
	<b>Sessions</b>	
14:40-16:45	Session 4: RF Techniques	M9-006
	Session 5: Systems on Chip (I)	Biruni Hall (M9-007)
	Session 6: Wireless Systems	M9-008
16:45-17:00	<b>Afternoon Coffee Break</b>	
	<b>Sessions</b>	
17:00-19:00	Session 7: CAD for VLSI (I)	M9-006
	Session 8: Device Characterization and Modeling	Biruni Hall (M9-007)
	Session 9: Embedded Systems	M9-008

Tuesday 16<sup>th</sup> of December 2008

<b>Time</b>	<b>Event/Session</b>	<b>Location</b>
9:30-10:15	Keynote 2 “Microprocessor Development: Retrospective and Future Challenges”	Biruni Hall (M9-007)
10:15-10:45	<b>Morning Coffee Break</b>	
	<b>Sessions</b>	
	Session 10: Analog Circuit Techniques (II)	M9-006
10:45-12:50	Session 11: Digital Circuits	Biruni Hall (M9-007)
	Session 12: MEMS and Devices	M9-008
12:50-14:15	<b>Lunch</b>	
	<b>Sessions</b>	
	Session 13: RF Circuits	M9-006
14:15-16:15	Session 14: VLSI Design for Communications	Biruni Hall (M9-007)
	Session 15: Topics on Nanoelectronics/Nanotechnology	M9-008
16:15-16:40	<b>Afternoon Coffee Break</b>	
	<b>Sessions</b>	
	Session 16: Testing and Verification	M9-006
16:40-18:30	Session 17: Data Converters	Biruni Hall (M9-007)
	Session 18: CAD for VLSI (II)	M9-008
19:30-23:00	<b>Banquet</b>	

Wednesday 17<sup>th</sup> of December 2008

<b>Time</b>	<b>Event/Session</b>	<b>Location</b>
9:30-10:15	Keynote 3 “Integrated Circuit Brokers: Where do they come from? Where are they heading?”	Biruni Hall (M9-007)
10:15-10:45	<b>Morning Coffee Break</b>	
10:45-12:45	<b>Sessions</b>	
	Session 19: Device Innovation and Applications	M9-006
	Session 20: Topics on Processor Design	Biruni Hall (M9-007)
	Session 21: Clock and Data Recovery	M9-008