

The 4th International Electronics Design Contest for Students IEDCS'07

I. OBJECTIVES

- Innovation Promotion
- Technical Knowledge Sharing
- Talents Exploration
- Academia and Industry Bridging

II. JOINING CRITERIA

- Innovative and applicable projects
- Designed by full-time undergraduate students
- Academic Year, Fall 2006/Spring 2007
- International submissions are invited

III. AWARDS

First Three ranked designs awards:

- Paper at IEEE ICM'07 Proceedings
- Presentation at IEEE ICM'07
- Waiving ICM'07 registration fees.
- "OpenTech" EDA tools and designs per winner.

IV. DESIGNS' SCOPE

Conceptual and Operational designs for:

- Digital/Analogue Integrated Circuits
- FPGA based designs, SOC
- Computer Architectures/ Processors
- Reconfigurable Computing Systems
- Embedded Systems, MEMS/Optics/Bio-Chips
- Control Systems, Telecommunications Systems.
- DSP Applications, Systems Software Modelling.
- Measurements and Instrumentation Systems.
- Design Methodologies/Verification Techniques.

V. DESIGN SUBMISSION

- Summary is in *IEEE journal two-column format*
- Summary doesn't exceed four pages
- Submit summary in PDF format by email
- Submission language is English
- Submission dead line is **August 15, 2007**
- Send design to: iedcs@handasarabia.org

VI. JUDGING

*International judging panel from academia,
industry plus the IEEE ICM'07 committee.*

Judging Criteria

- Originality
- Soundness of Engineering
- Documentation Quality and Clarity
- Performance
- Judging decision by: September 1, 2007

**In Cooperation with the 19th IEEE International
Conference on Microelectronics, ICM 2007,
December 2007, Egypt**

*EETimes & EEdesign News Story about IEDCS'04
August, 04 2004*

www.eedesign.com/article/printableArticle.jhtml?articleID=26805904

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IEEE ICM'07 Conference

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